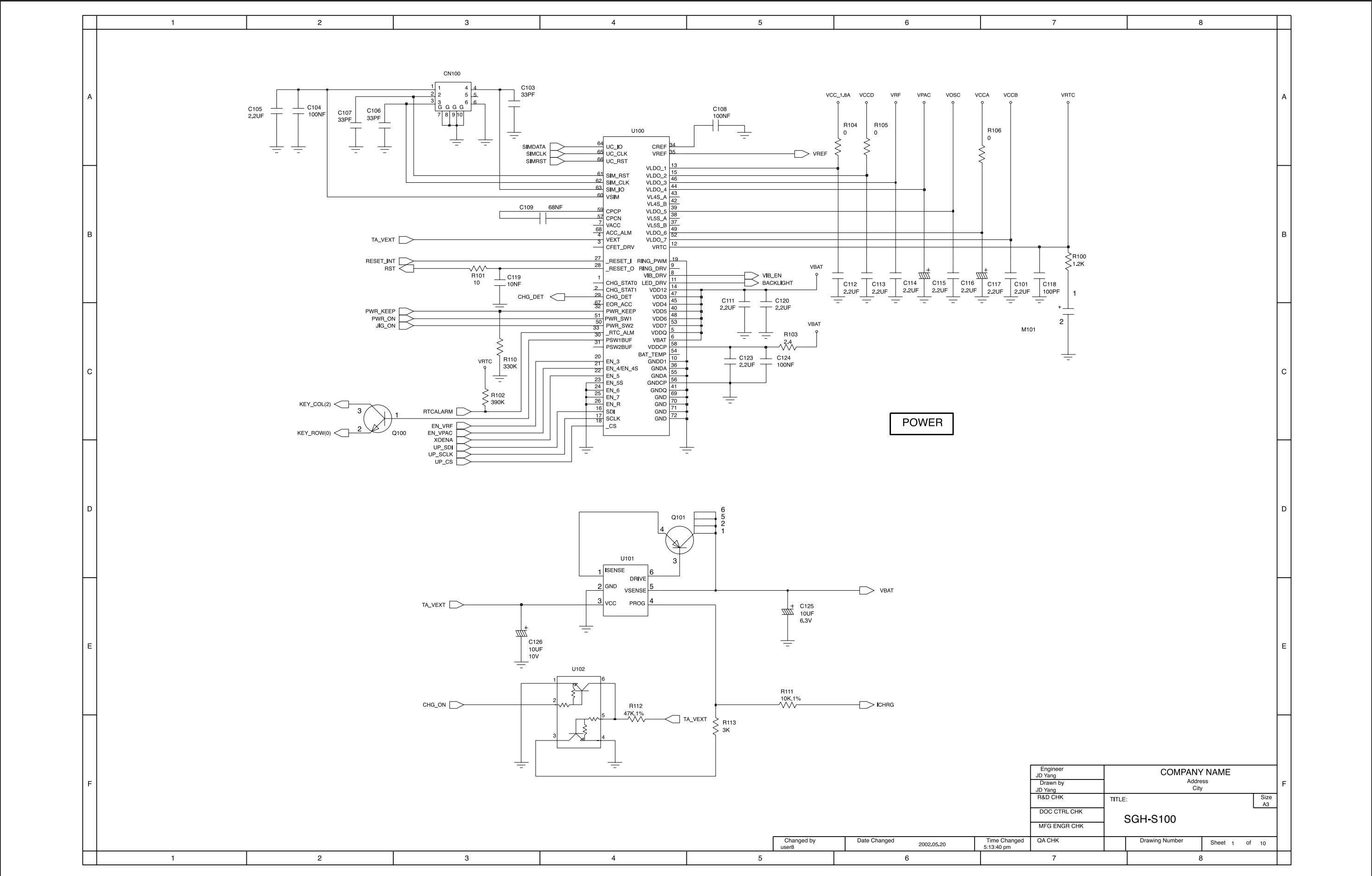
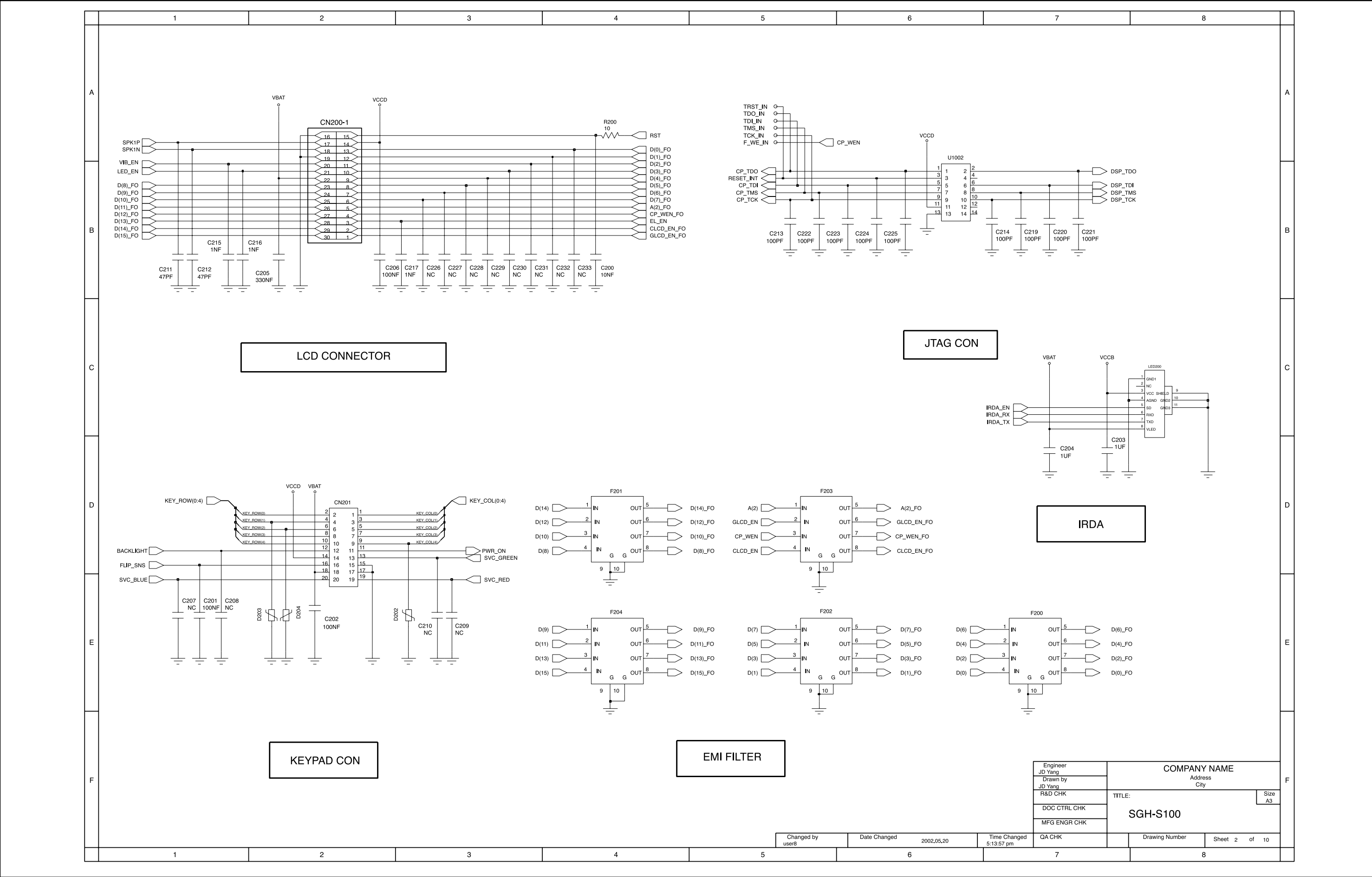


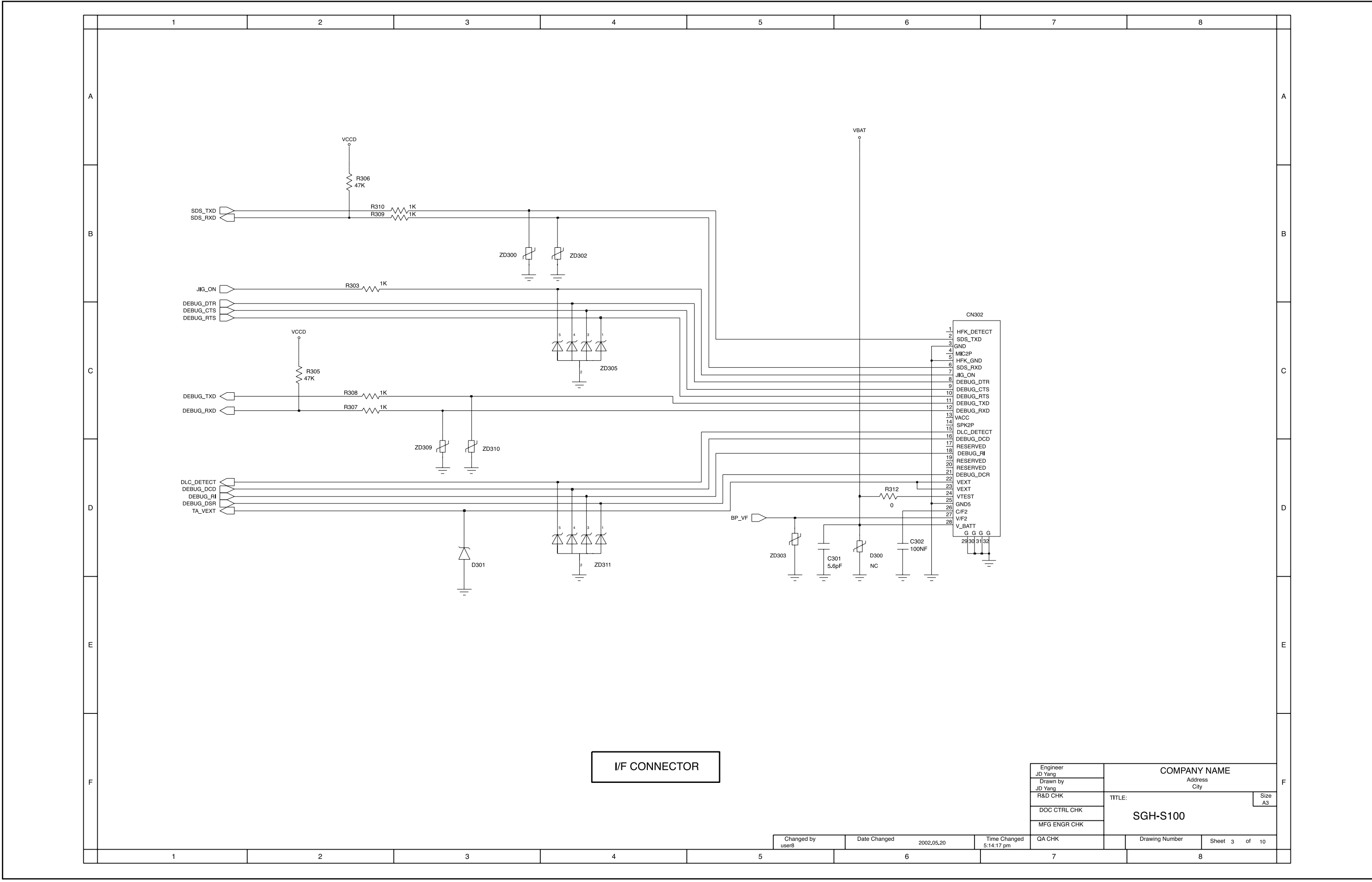
5-15. Power Circuit Diagram



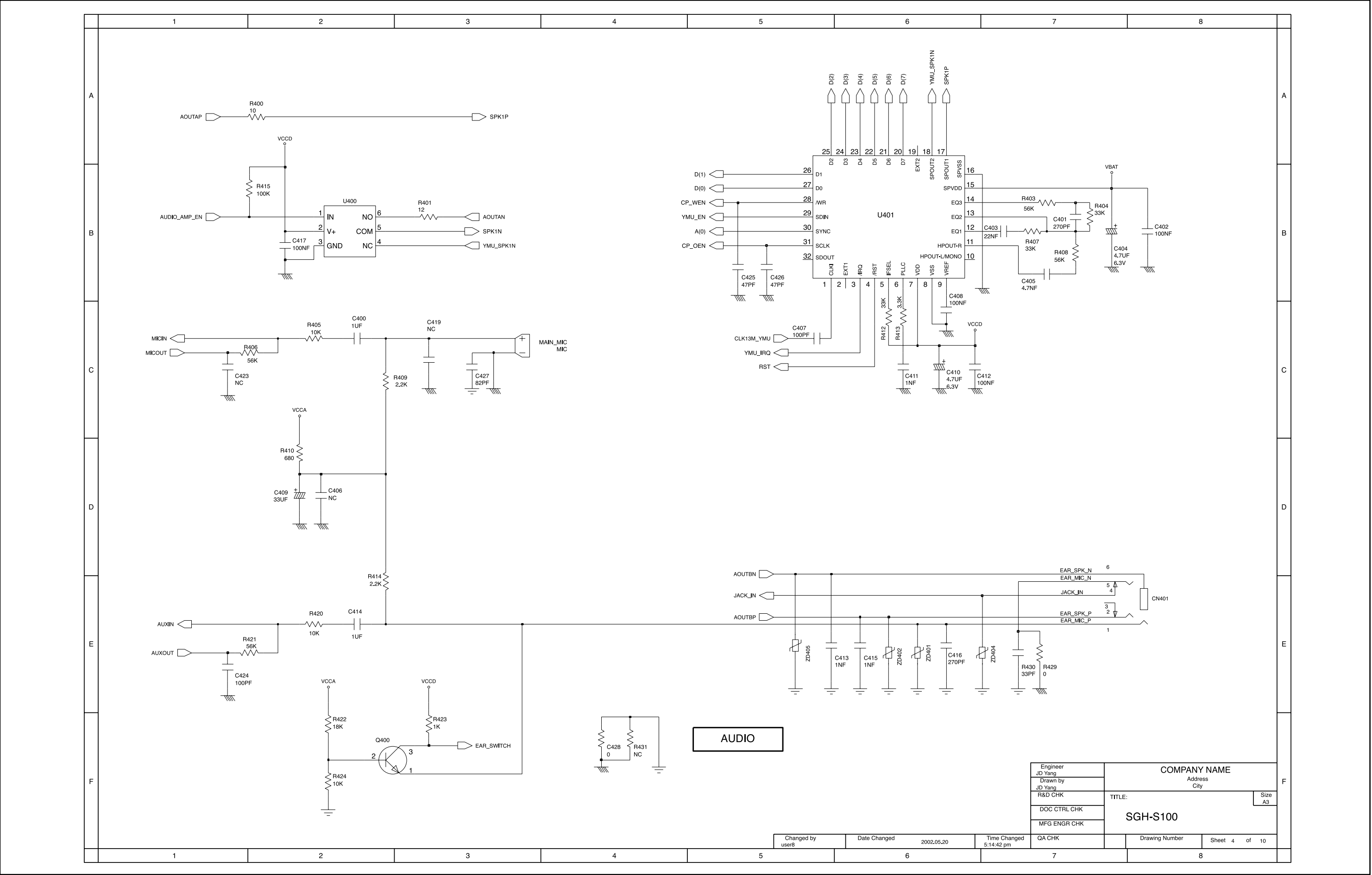
5-16. LCD & KEYPAD & EMI FILTER & JTAG & IRDA Connector Circuit Diagram



5-17. I/F Connector Circuit Diagram



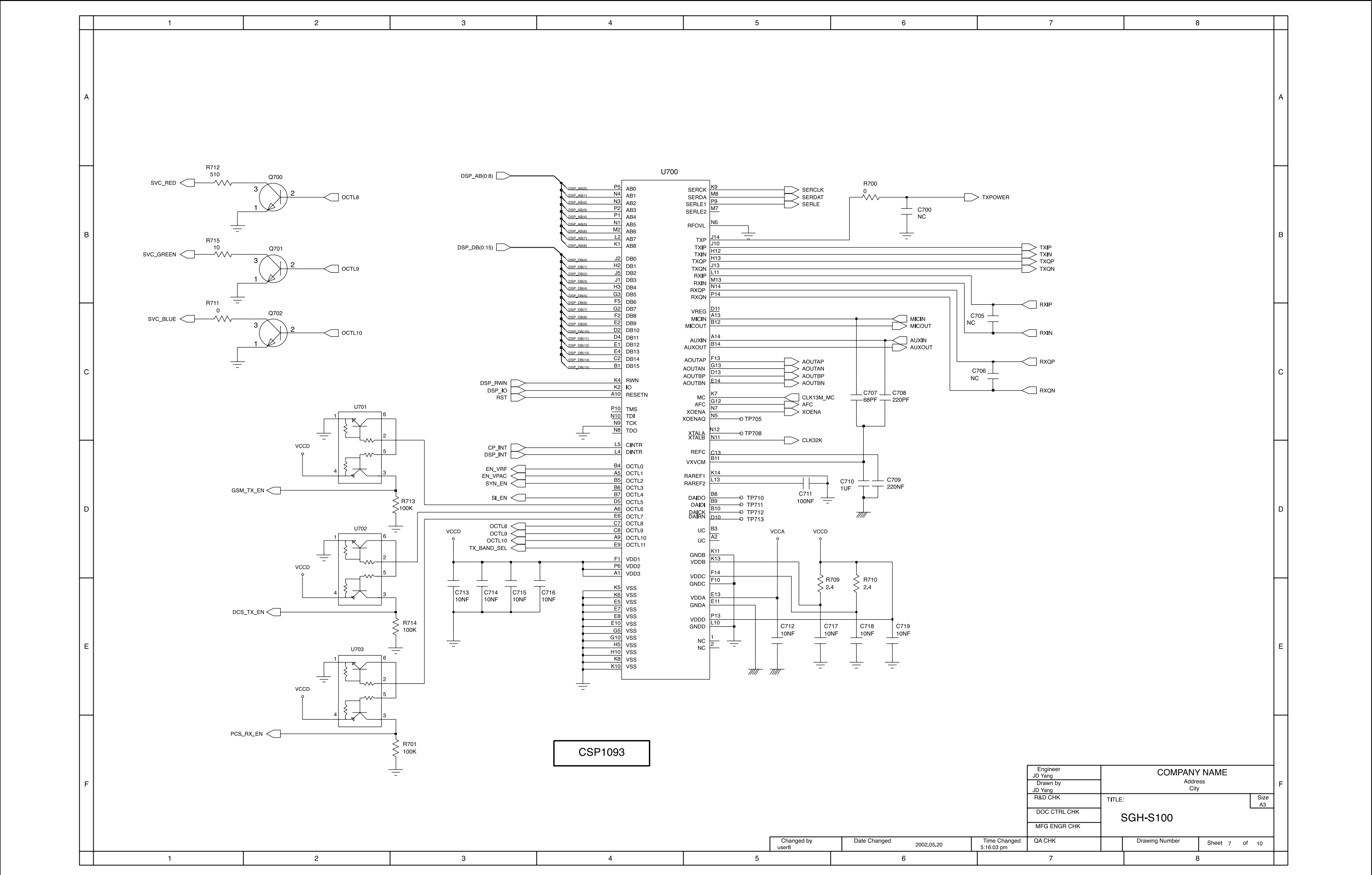
5-18. Audio Circuit Diagram







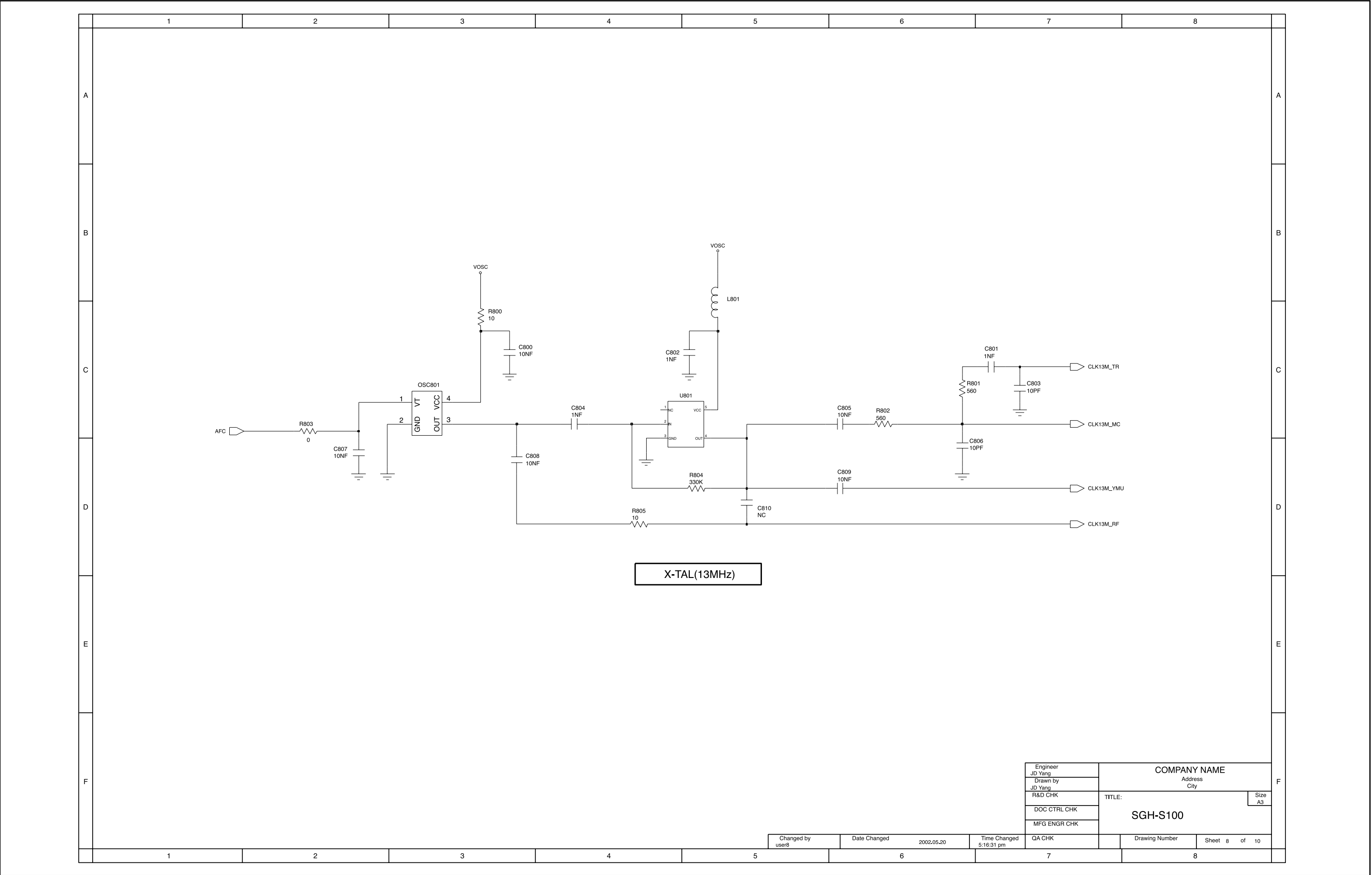
5-21. CSP1089 Circuit Diagram

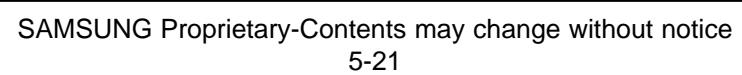


Engineer JD Yang	COMPANY NAME Address City		Size A3
Drawn by JD Yang			
R&D CHK	TITLE: SGH-S100		
DOC CTRL CHK			
MFG ENGR CHK			
QA CHK	Drawing Number	Sheet 7 of 10	

Changed by user8	Date Changed 2002.05.20	Time Changed 5:16:03 pm
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5-22. X-TAL(13 MHz) & SIM Circuit Diagram





5-24. Transmitter & Receiver Circuit Diagram

